



UPPAAL TUTORIAL

Modeling Real-Time Systems

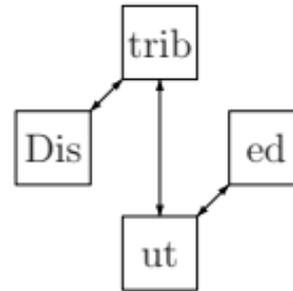
01.02.2018

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ABOUT ME

- Ph.D.

- Model based testing of



systems.

<http://research-deepak.com>



WHAT IS A MODEL?

- A model is a description of a system's behavior.
- Behavior can be described in terms of input sequences, actions, conditions, output and flow of data from input to output.
- It should be practically understandable and can be reusable; shareable must have precise description of the system under test.



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- Can you name some model formalisms to describe different aspects of system behavior ?
 - FSM, State chart, UML, Decision Tables, Timed Automata .. etc



UPPAAL INTRODUCTION

- UPPAAL is a tool for modeling, validation (via graphical simulation) and verification (via automatic model-checking) of real-time systems.
- Appropriate for systems that can be modeled as a collection of non-deterministic processes with finite control structure and real-valued clocks (i.e. timed automata)
- UPPAAL = UPP (Uppsala University) + AAL (Aalborg University).
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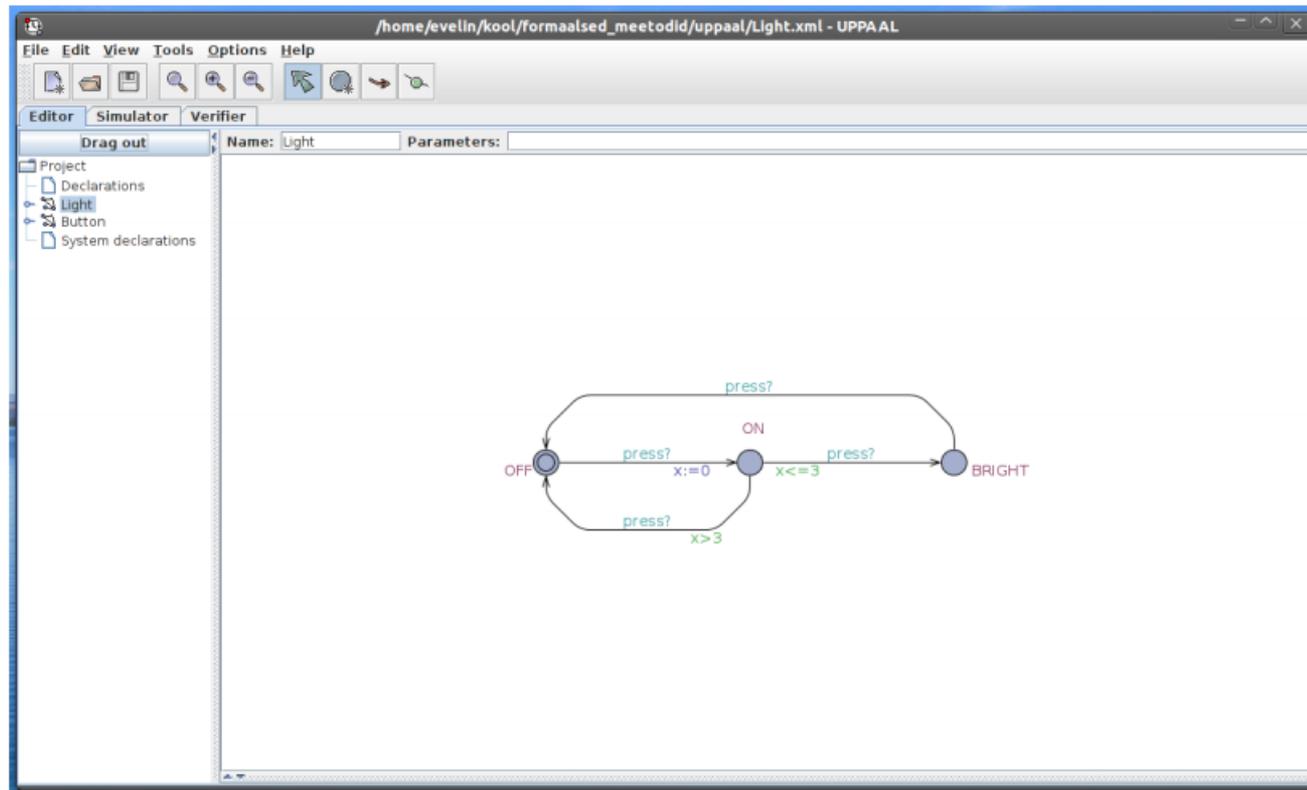


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EDITOR



SIMULATOR

The screenshot displays the UPPAAL simulator interface for a file named `uppaal/Light.xml`. The interface is divided into several sections:

- Enabled Transitions:** A list showing the transition `press: B -> L` as the currently enabled transition.
- Simulation Trace:** A log of simulation events, including state changes and transition firings, such as `(OFF, -)`, `press: B -> L`, `(ON, -)`, and `(OFF, -)`.
- Simulation Trace File:** A section with buttons for `Prev`, `Next`, `Replay`, `Open`, `Save`, and `Auto`, along with a speed control slider ranging from `Slow` to `Fast`.
- Model View:** A Petri net diagram with places `L` and `B`. Place `L` contains 3 tokens. Transitions are labeled `press?`. Guards include `x=0`, `x<=3`, and `x>3`. An output place `BRIGHT` is also shown.
- Synchronization History:** A vertical timeline showing the sequence of synchronization points between the two processes, with labels `L` and `B` indicating the active process.

Variable values

System

Controls

Synchronization History



SIMULATION

- Step-by-step simulation –
 - Good for observations of variable values at each step -
Manually selecting transitions (when many are enabled)
 - Good for tracing errors
- Automatic simulation - Good for observing overall system behavior
- Saving/Opening Simulation Traces



LOCATIONS

Locations

Locations can have an optional name. The name must be a valid identifier.

Exactly one per Template

Like urgent locations, committed locations freeze time. Furthermore, if any process is in a committed location, the next transition must involve an edge from one of the committed locations.

Dialog box titled "Edit Location" with tabs "Location" and "Comments".

Fields:

- Name: ON
- Invariant: $x \leq 2$
- Initial
- Urgent
- Committed

Buttons: OK, Cancel

Conjunction of simple conditions on clocks, differences between clocks, and boolean expressions not involving clocks. The bound must be given by an integer expression. Lower bounds on clocks are disallowed. States which violate the invariants are undefined; by definition, such states do not exist.

Freeze time; *i.e.* time is not allowed to pass when a process is in an urgent location.



EDGES

Selections are randomized initialization of some variable in a range whenever an edge is executed. The other three labels of an edge are within the scope of this binding. E.g., "i: int[3,5]" – randomly set i to be between 3 to 5, inclusively.

When executed, the update expression of the edge is evaluated. The side effect of this expression changes the state of the system.

Edge Comments

Select:

Guard: `x <= 3`

Sync: `press?`

Update:

OK Cancel

An edge is enabled in a state if and only if the guard evaluates to true.

Processes can synchronize over channels. Edges labeled with complementary actions over a common channel synchronize (press! press?).



DECLARATION

- Declarations are either global or local (to a template) and can contain declarations of clocks, bounded integers, channels (although local channels are useless), arrays, records, and types.

- `chan d;`

a channel.

- `urgent chan e;`

an urgent channel.

- `struct { int a; bool b; } s1 = { 2, true };`

an instantiation of the structure from above where the members `a` and `b` are set to `2` and `true`.



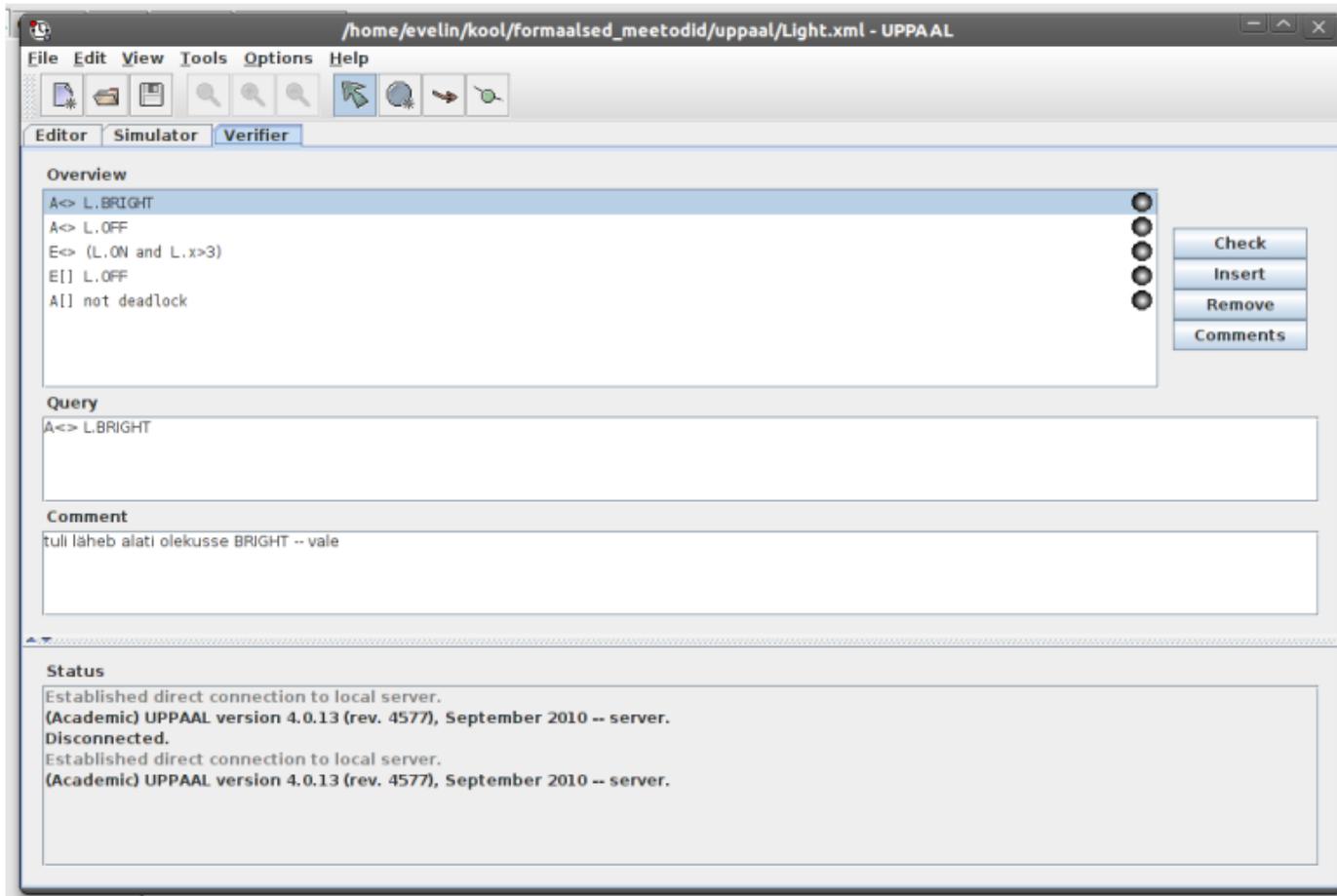
- `const int a = 1;`
constant `a` with value 1 of type integer.
- `bool b[8], c[4];`
two boolean arrays `b` and `c`, with 8 and 4 elements respectively.
- `int[0,100] a=5;`
an integer variable with the range [0, 100] initialised to 5.
- `int a[2][3] = { { 1, 2, 3 }, { 4, 5, 6 } };`
a multidimensional integer array with default range and an initialiser.
- `clock x, y;`
two clocks `x` and `y`.

More information:

<http://www.it.uu.se/research/group/darts/uppaal/help.php?file=Introduction.shtml>



VERIFIER



The query language of Uppaal, used to specify properties to be checked, is a subset of TCTL (timed computation tree logic)



QUERY IN UPPAAL

- E - exists a path (“E” in UPPAAL).
- A - for all paths (“A” in UPPAAL).
- [] – all states in a path
- <> - some states in a path

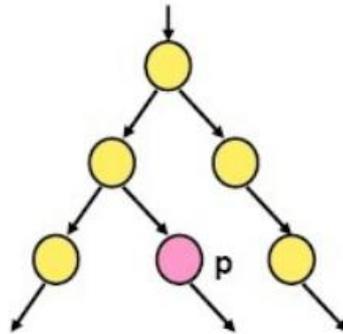
The following combination are supported:

- **A[], A<>, E<>, E[].**



$E \langle \rangle P$ – “P REACHABLE”

$E \langle \rangle p$ – it is possible to reach a state in which p is satisfied.

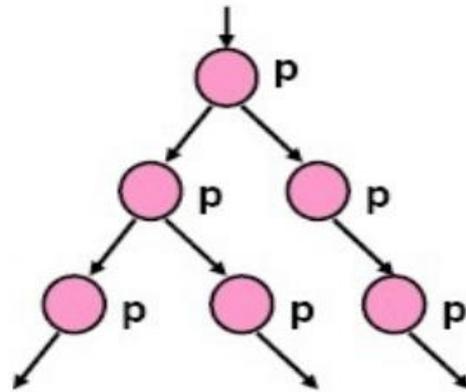


P is true in (at least) one reachable state.



$A[] P$ – “INVARIANTLY P”

$A[] p$ – p holds invariantly.



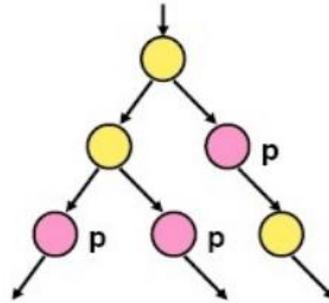
P is true in all reachable states.



$A \langle \rangle P$ – “INEVITABLE P”

$A \langle \rangle p$ – p will inevitably become true

The automaton is guaranteed to eventually reach a state in which p is true.

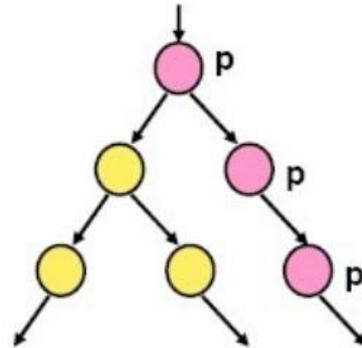


P is true in some state of all paths.



$E[] P$ – “POTENTIALLY ALWAYS P”

$E[] p$ – p is potentially always true.

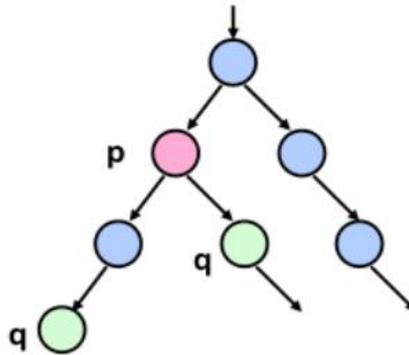


There exists a path in which P is true in all states.



$P \rightarrow Q$ – “P LEAD TO Q”

$p \rightarrow q$ – if p becomes true, q will inevitably become true.



Same as $A \rightarrow B$ ($p \text{ imply } A \leftrightarrow q$)



SPECIFYING PROPERTIES

- **A[] not deadlock**
 - no deadlocks
 - true
- **E[] L.OFF**
 - is it possible that the the light is always OFF
 - true
- **E<> (L.ON and L.x >3)**
 - it is possible that the light isn't pressed a second time within 3 seconds after it's turned on
 - true
- **A<> L.OFF**
 - no matter how your operate the light, it will go to OFF
 - true
- **A<> L.BRIGHT**
 - no matter how your operate the light, it will go to BRIGHT
 - false



LIGHT CONTROLLER EXAMPLE

- Model the abstract behavior of a simple light controller switch shown in fig below.



REQUIREMENTS

- The lamp has three locations: OFF, ON, and BRIGHT.
- If the user presses a button, i.e., synchronizes with `press?`, then the lamp is turned on.
- If the user presses the button again, the lamp is turned off.
- However, if the user is fast and rapidly presses the button twice, the lamp is turned on and becomes bright.
- The user can press the button randomly at any time or even not press the button at all. The clock `x` of the lamp is used to detect if the user was fast ($x \leq 3$) or slow ($x > 3$).



NEXT LAB:

- Urgent and Committed Locations
- Urgent Channels
- Broadcast Synchronization .
- Networks of Timed Automata
- Model Checking

